

Attorney's Docket No. 5288.P002

PATENT

2151
#2
BR
10-4-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Varaprasad Vajjhala, et al.

Examiner: Not Yet Assigned

Application No.: 09/977,511

Art Unit: 2151

Filed: October 12, 2001

For: METHOD AND SYSTEM TO
ALLOCATE RESOURCES WITHIN AN
INTERCONNECT DEVICE ACCORDING
TO A RESOURCE ALLOCATION TABLE

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Assistant Commissioner for Patents
Washington, D.C. 20231

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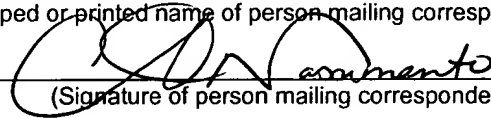
INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D. C. 20231 on September 24, 2002.
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Carla A. Nascimento
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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

 X 37 C.F.R. §1.97(b).

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 A statement pursuant to 37 C.F.R. §1.97(e) or

 A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

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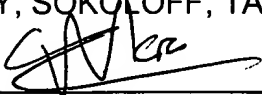
- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
- (2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 07/24/2002



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Substitute for Form 1449A/PTO (Modified) <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center; font-size: small;">(use as many sheets as necessary)</p>		Complete if Known	
		Application Number	09/977,511
		Filing Date	October 12, 2001
		First Named Inventor:	Varaorasad Vajhala, et al.
		Group Art Unit	2151
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	5288.P002

OTHER ART – NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		"InfiniBand Switch Chip Runs at 10 Gbps On Eight Ports ", Nicholas Cravotta, 11/8/2001, EDN, 1 page	
		"Assemble Fast Switch Fabrics With 32-Port InfiniBand Node p. 60", Electronic Design, 10/15/2001, 4 pages	
		"RedSwitch, Inc. Announces Industry's Highest Performance and Highest Integration InfiniBand Switch Chip", RedSwitch Press Release, 10/16/2001, 2 pages.	
		"RedSwitch Gearing Up To Launch New Chip", Steve Tanner, Silicon Valley Business Ink, 10/26/2001, 3 pages.	
		"Mellanox Integrates Serdes Into Infiniband Switch", Jerry Ascierio, EE Times, 10/23/2001, 3 pages.	
		"Switch Chip Expands InfiniBand Integration", EEM File 3130, Tony Chance, 2 pages.	
		"RedSwitch Announces 16 Gbyte/s Throughout Switch Product for RapidIO Architecture", RedSwitch Press Release, Milpitas, Calif., May 15, 2001, Tony Chance, 5/15/2001, 2 pages.	
		"RedSwitch and Agilent Technologies Unveil 160-GB/s Throughout Switch Product for InfiniBand Architecture", RedSwitch Press Release, Intel Developer Forum Conference, San Jose, Calif., Feb. 27, 2001, Mark Alden-Agilent, Tony Chance-RedSwitch, 2 pages.	

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Examiner Signature	Date Considered
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